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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/632,452	08/04/2000	Jeff S. Ford	1247/A54	1645
22801	7590	06/10/2003		
LEE & HAYES PLLC 421 W RIVERSIDE AVENUE SUITE 500 SPOKANE, WA 99201				EXAMINER RAHMJOO, MANUCHER
			ART UNIT 2676	PAPER NUMBER

DATE MAILED: 06/10/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/632,452	FORD ET AL.
	Examiner	Art Unit
	Mike Rahmjoo	2676

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) Responsive to communication(s) filed on 21 May 2003.
- 2a) This action is FINAL.      2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) Claim(s) 1-61 is/are pending in the application.
  - 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) \_\_\_\_\_ is/are allowed.
- 6) Claim(s) 1-61 is/are rejected.
- 7) Claim(s) \_\_\_\_\_ is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.
 

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.
 

If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) All
  - b) Some \*
  - c) None of:
    - 1) Certified copies of the priority documents have been received.
    - 2) Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
    - 3) Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.
- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
  - a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____.
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>6,10</u> .	6) <input type="checkbox"/> Other: _____.

## DETAILED ACTION

### *Response to Arguments*

Applicant's arguments with respect to claims 1- 45 have been considered but are moot in view of the new ground(s) of rejection.

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1- 21, 23- 45, 52, and 57 are rejected under 35 U.S.C. 102(b) as being anticipated by Carlucci et al (US Patent 5,191,645), hereinafter, Carlucci.

As per claims 1, 23, 44, 52 and 57 Carlucci teaches a receiver for receiving a video signal forwarded from a video signal source within the video graphic workstation see for example figure 2 block 70 and figure 3 block 100; a video pipeline for post-processing the received video signal, the video pipeline producing a post-processed video signal see for example figure 2 through block 72 and figure 3 block 102; and a video output module for converting the post-processed video signal, the video output module producing a formatted video signal see for example figure 2 through block 74 and figure 3 blocks 104, 106, and 108 wherein the video signal source is a storage medium, a video graphics processor and a video signal input system see for example figure 1 through block 70 and figures 3- 4.

As per claims 2, 24 and 45 Carlucci teaches an ancillary data injector, the injector inserting

ancillary data into the post-processed video signal in figure 2 through block 34 (VTR).

As per claims 3, 4, 25 and 26 Carlucci teaches a generator locking device in figures 8 and 9 through blocks 190 and 180 respectively.

As per claims 5 and 27 Carlucci teaches e-VS is an RGB encoded video signal, an RGBA encoded video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal in column 3 line 48 wherein motion picture film (video) in color or black and white is received by camera processor 12.

As per claims 6- 8 and 28- 30 Carlucci teaches the video signal source is a storage medium, a video graphics processor and a video signal input system see for example figure 1 through block 70 and figures 3- 4.

As per claims 9 and 31 Carlucci teaches VS is an analog composite video signal, an analog component video signal, a serial digital composite video signal, a serial digital component video signal, a parallel digital composite video signal, or a parallel digital component video signal in column 7 lines 56- 65 wherein RGB are read out in parallel from buffers to produce three parallel data streams.

As per claims 10 and 32 Carlucci teaches the process of post-processing includes region of interest selection in column 12 lines 32- 38 wherein portion of an image is selected.

As per claims 11 and 33 Carlucci teaches the process of post-processing includes frame rate matching in column 6 lines 25- 37 wherein in order to achieve the desired four fold increase in frame rate each bit written into each one of the buffers has to be read out four times from that buffer.

As per claims 13 and 35 Carlucci teaches the process of spatial adaptation includes scaling in column 5 lines 54- 60 wherein the resolution is enhanced.

As per claims 12, 14, 15, 34, 36, and 37 Carlucci teaches the process of picture framing includes letter boxing and the process of spatial adaptation includes spatial adaptation and picture framing in column 12 lines 32- 38 wherein portions of images are selected and occupy distinct portion of the monitor.

As per claims 16 and 38 Carlucci teaches the process of post-processing includes changing the sample rate of the video signal being post-processed in column 3 lines 54- 60 wherein camera processor generate high definition video signal different than 1.875 fps.

As per claims 17, 18, 39, and 40 Carlucci teaches the process of post-processing includes gamma insertion and removal in column 9 lines 17- 29 wherein gamma correction is made.

As per claims 19 and 41 Carlucci teaches the process of post-processing includes color space conversion in column 8 lines 40- 52 wherein color transformation of digitized images is made.

As per claims 20 and 42 Carlucci teaches the process of post-processing includes changing frames of video data into interleaved fields of video data in column 5 line 20 through alternating frames stored.

As per claims 21 and 43 Carlucci teaches the process of post-processing includes addressing on a frame-by-frame basis the video signal being post-processed in column 5 line 9.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Carlucci in view of Olarig et al (US Patent 5,937,173), hereinafter, Olarig.

As per claim 22 Carlucci does not teach the system is a Peripheral Component Interconnect circuit board.

However, Olarig teaches the system is a Peripheral Component Interconnect circuit board in column 8 lines 2- 8.

It would have been obvious to one of ordinary skilled in the art at the time the invention was made to incorporate the teachings of Olarig into Carlucci to make dual use of signal pins on the multiple set core logic chip set which may reduce the number of overall pin count to result into reduction of manufacturing costs in column 8 lines 5- 10.

Claims 46,47,49 and 50 are rejected under 35 U.S.C. 103(a) as being unpatentable over Carlucci in view of Kostreski et al (US Patent 5,734,589), hereinafter, Kostreski.

As per claims 46,47,49 and 50 Carlucci does not teach the video output module is a daughter board.

However, Kostreski teaches a daughter board see for example column 9 lines 1- 21 and figure 1.

It would have been obvious to one of ordinary skilled in the art at the time the invention was made to incorporate the teachings of Kostreski into Carlucci for a technician to replace the module in either the field or the shop so as modify a DET to connect to and communicate over a different network or other implementations further described.

As per claims 48 and 51 Carlucci does not teach the video output informing of its configuration.

However, it is well known in the art to inform of a configuration of any detachable module to the main processor (CPU commonly on the mother board) so as to make and utilize an operational piece of hardware/ software.

Claims 53- 56 and 58- 61, individually or in combination, are substantially the similar to claims 1- 52 and are therefore rejected with the same rational.

***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

The following patents are cited to further show the state of the art with respect to post processing the received video signal:

US Patent 6,314,569

US Patent 5,353,119

US Patent 6,433,796

US Patent 5,900,917

US Patent 6,130,600

US Patent 6,088,045

US Patent 5,027,212

**Inquiry**

**THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

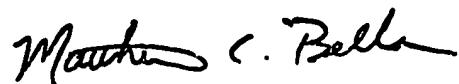
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mike Rahmjoo whose telephone number is (703) 305- 5658. The examiner can normally be reached on 6:30- 3:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Bella can be reached on (703) 308- 6829. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872- 9314 for regular communications and (703) 872- 9314 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305- 4750.

Mike Rahmjoo

June 4, 2003



MATTHEW C. BELLA  
SUPERVISORY PATENT EXAMINER  
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